IN THE CLAIMS

1. (Currently Amended) A method for detecting the bus width of a peripheral device connected to an electronic device, wherein at least one bus width from a determined set of bus widths is available in the peripheral device, wherein for detecting the bus widths available in the peripheral device, one or more indicators formed in the peripheral device are used, which one or more indicators is itself or are themselves only indirectly indicative of ~~indicate~~ which one or ones of said set of bus widths are available in the peripheral device so as to avoid or reduce memory otherwise employed by storage of a direct indicator of said at least one bus width.

2. (Original) The method according to claim 1, wherein reference data is stored in the electronic device about at least one bus width available in the peripheral device and corresponding to said indicator value.

3. (Original) The method according to claim 2, wherein said indicator used is information stored in the peripheral device and indicating indirectly, which one or ones of said set of bus widths are available in the peripheral device.

4. (Original) The method according to claim 3, wherein said data stored in the peripheral device is information about the maximum clock frequency available in the peripheral device.

5. (Original) The method according to claim 3, wherein at least a fast peripheral device and a slow peripheral device are defined, wherein said information stored in

the peripheral device is information about whether the peripheral device is fast or slow.

6. (Original) The method according to claim 3, wherein said data stored in the peripheral device is information about the version of the peripheral device.

7. (Original)The method according to claim 2, comprising performing at least the following steps:
a request step, in which a request is transmitted from the electronic device to the peripheral device to transmit the value of said indicator to the electronic device,
a reply step, in which said indicator value is transmitted from the peripheral device to the electronic device,
an identification step, in which said indicator value is compared with at least one reference value stored in the electronic device,
a selection step for selecting one bus width available in the peripheral device, and
a setting step for setting the selected bus width for the peripheral device.

8. (Original) The method according to claim 1, wherein at least one connection line is formed between the electronic device and the peripheral device, and using at least one said connection line as said indicator.

9. (Original) The method according to claim 8, comprising performing at least the following steps:

an initialization step, in which the value of said at least one connection line is set to correspond indirectly to the bus widths available in the peripheral device,

a detection step, in which the electronic device examines the state of said at least one connection line and compares the state of said connection line with at least one reference value stored in the electronic device,

a selection step for selecting one bus width available in the peripheral device, and

a setting step for setting the selected bus width for the peripheral device.

10. (Currently Amended) A system comprising an electronic device, a peripheral device which can be connected to the electronic device and in which at least one bus width is arranged to be used from a defined set of bus widths, and which system comprises a bus width detector for detecting at least one bus width available in the peripheral device connected to the electronic device, wherein the peripheral device is provided with one or more indicators, which one or more indicators is itself or are themselves only indirectly ~~arranged to indicate~~indicative of which one or ones from said set of bus widths are available in the peripheral device so as to avoid or reduce memory otherwise employed by storage of a direct indicator of said at least one bus width.

11. (Currently Amended) An electronic device comprising a bus width detector for detecting the bus width of a peripheral device connected to the electronic device, in which peripheral device at least one bus width is arranged to be used from a defined set of bus widths, the detector also comprising means for determining the value of one or more indicators formed in the peripheral device, which ~~indicator is~~

~~arranged~~one or more indicators is ~~to~~ itself or themselves only indirectly ~~indicate~~ indicative of which one or ones of said set of bus widths are available in the peripheral device so as to avoid or reduce memory otherwise employed by storage of a direct indicator of said at least one bus width.

12. (Original) The electronic device according to claim 11, wherein reference data is stored in the electronic device about at least one bus width available in the peripheral device and corresponding to said indicator value.

13. (Original) The electronic device according to claim 12, wherein said indicator arranged to be used is information stored in the peripheral device and indicating indirectly, which one or ones of said set of bus widths are available in the peripheral device.

14. (Original) The electronic device according to claim 13, wherein at least one connection line is formed between the electronic device and the peripheral device, and that said indicator arranged to be used is at least one said connection line.

15. (Original) The electronic device according to claim 14, said detector comprising means for examining the value of said connection line.

16. (Currently Amended) A peripheral device which can be connected to an electronic device comprising a bus width detector for detecting the bus width of the peripheral device connected to the electronic device, and in which peripheral device

at least one bus width from a defined set of bus widths is arranged to be used, wherein the peripheral device is provided with one or more indicators which ~~are arranged to~~is itself or are themselves only indirectly ~~indicate~~indicative of which one or ones of said set of bus widths are available in the peripheral device so as to avoid or reduce memory otherwise employed by storage of a direct indicator of said at least one bus width.

17. (Original) The peripheral device according to claim 16, wherein information about the maximum clock frequency available in the peripheral device is stored in the peripheral device.

18. (Original) The peripheral device according to claim 16, wherein at least a fast peripheral device and a slow peripheral device have been defined, wherein information about whether the peripheral device is fast or slow is stored in the peripheral device.

19. (Original) The peripheral device according to claim 16, wherein information about version of the peripheral device is stored in the peripheral device.

20. (Original) The peripheral device according to claim 16, comprising at least one connection line, and means for setting said connection line in a value which indirectly corresponds to the bus widths available in the peripheral device.

21. (Currently Amended) A memory card which can be connected to an electronic device comprising a bus width detector for detecting the bus width of the memory card connected to the electronic device, and in which memory card at least one bus width from a defined set of bus widths is arranged to be used, wherein the memory card is provided with one or more indicators which ~~are arranged to~~is itself or are themselves only indirectly ~~indicate~~indicative of which one or ones of said set of bus widths are available in the memory card so as to avoid or reduce memory otherwise employed by storage of a direct indicator of said at least one bus width.